

What is claimed is:

1. A line-on glass liquid crystal display panel, comprising:
a picture display part with a matrix of liquid crystal cells;
a plurality of line-on glass type signal lines located in an outer area of the picture display part for applying drive signals to drive the liquid crystal cells; and
a dummy line formed between the line-on glass type signal lines with at least a layer of insulating film therebetween.
2. The line-on glass liquid crystal display panel according to claim 1, further comprising:
first and a second line-on glass signal pads which extend from both sides of the line-on glass type signal line.
3. The line-on glass liquid crystal display panel according to claim 2, further comprising:
first and a second dummy pads that extend from both sides of the dummy line.
4. The line-on glass liquid crystal display panel according to claim 3, wherein the first and second dummy pads are located between the first and second line-on glass type signal pads.
5. The line-on glass liquid crystal display panel according to claim 1, wherein the line-on glass type signal lines are formed in a same layer as a gate line of the picture display part.

6. The line-on glass liquid crystal display panel according to claim 5, wherein the dummy line is formed in a same layer as a data line of the picture display part that crosses the gate line with a gate insulating film therebetween.

7. The line-on glass liquid crystal display panel according to claim 6, wherein the dummy line is located between the line-on glass type signal lines with the gate insulating film therebetween.

8. The line-on glass liquid crystal display panel according to claim 5, wherein the dummy line is formed in a same layer as a pixel electrode of the picture display part.

9. The line-on glass liquid crystal display panel according to claim 8, wherein the dummy line is located between the line-on glass type signal lines with a gate insulating film and a protective film therebetween, and the gate insulating film and the protective film are formed to cover the gate line.

10. The line-on glass liquid crystal display panel according to claim 1, wherein the dummy line transmits a common voltage.

11. The line-on glass liquid crystal display panel according to claim 1, wherein the dummy line transmits a ground voltage.

12. A fabricating method of a line-on glass liquid crystal display panel, comprising:

forming a plurality of line-on glass signal lines in an outer area of a picture display part;

forming at least one layer of insulating film to cover the

line-on glass type signal lines; and

forming a dummy line that is located between the line-on glass signal lines on the insulating film.

13. The fabricating method according to claim 12, further comprising:

forming a gate line of the picture display part on a substrate and a gate electrode connected to the gate line;

forming a gate insulating film on the substrate on which the gate line and the gate electrode are formed;

forming a semiconductor layer on the gate insulating film;

forming a data line crossing the gate line, a source electrode connected to the data line, and a drain electrode opposite to the source electrode with a designated gap therebetween, on the substrate on which the semiconductor is formed;

forming a protective film on the substrate where the data line, the source electrode and the drain electrode are formed; and

forming a pixel electrode connected to the drain electrode on the protective film.

14. The fabricating method according to claim 13, wherein the line-on glass signal line is formed of a same metal as a gate line.

15. The fabricating method according to claim 14, wherein the dummy line is formed of a same metal as the data line.

16. The fabricating method according to claim 15, wherein the dummy line is formed between the line-on glass type signal lines with the gate insulating film therebetween.

17. The fabricating method according to claim 14, wherein the dummy line is formed of a same metal as a pixel electrode.

18. The fabricating method according to claim 17, wherein the dummy line is formed between the line-on glass type signal lines with the gate insulating film and the protective film therebetween.